

SECUENCIA DE ENCENDIDO POR ETAPAS



ETAPA 1

V_BATTERY
V_BAT

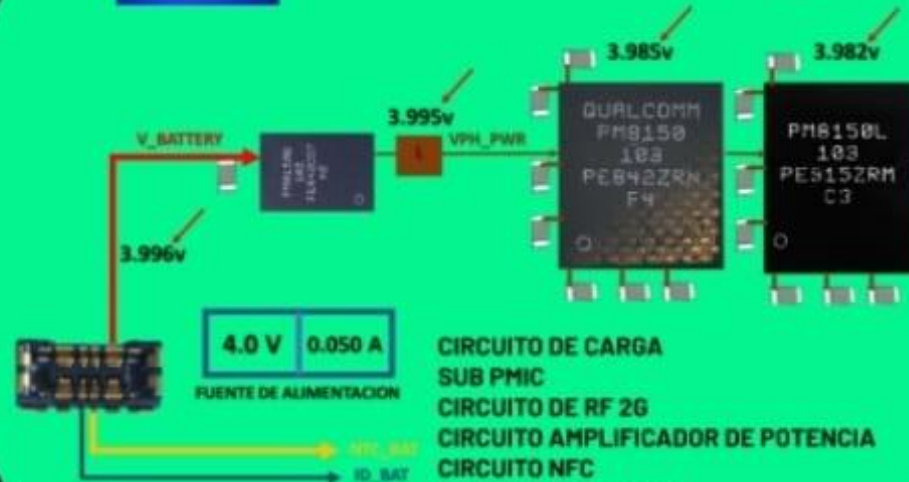
ETAPA 2

LINEAS BUCK
LINEAS LDO'S
CRISTAL DE
OSCILACION
ANALOGICA

ETAPA 3

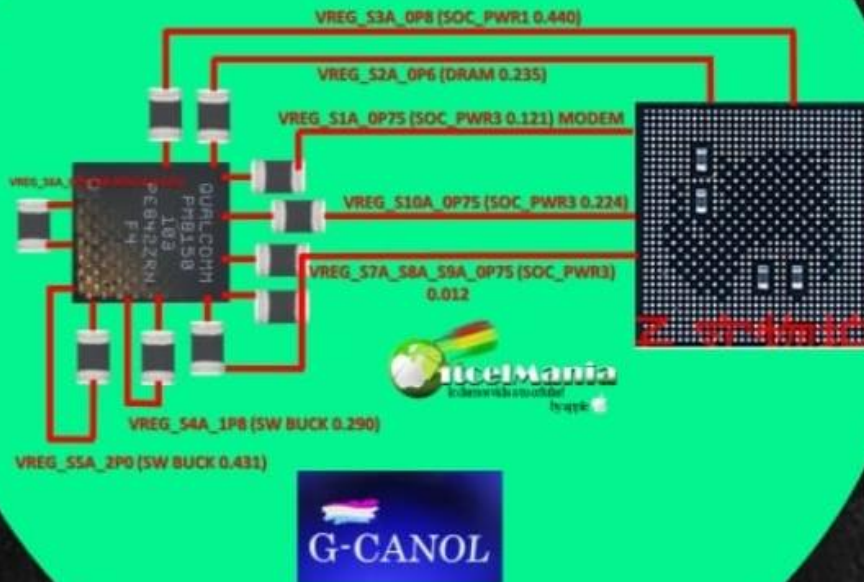
LINEAS DIGITAL
PMU1 A CPU
LINEAS CPU A
PMIC2, NAND,
RAM, SUB PMIC E
IC DE CARGA

V_BATTERY V_BAT O VPH_PWR



XTAL
(CRISTAL DE
OSCILACION
ANALOGICA)

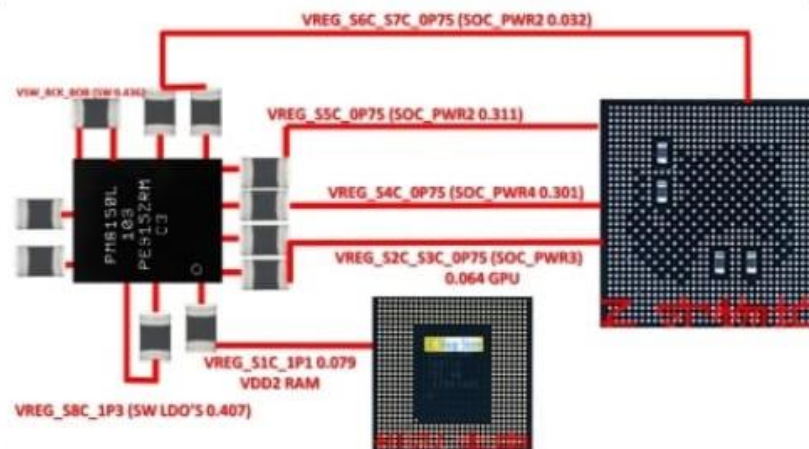
PMIC 1 (SW BUCK)



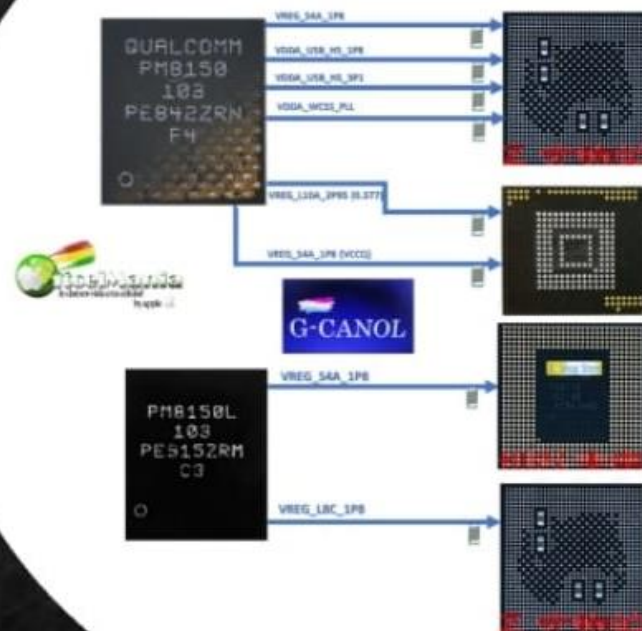
PMIC 2
(SW BUCK)

LDO'S

PMIC 2 BUCK CPU, RAM Y LDO



LDO'S



CPU
Y
NAND

PMIC - CPU DIGITAL



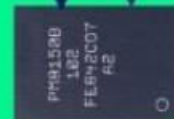
G-CANOL



MSM_RESIN_N (0.500) (1.8V)
MSM_PS_HOLD (0.478) (1.8V)
PMIC_SPMI_CLK (0.433) (1.8V)
PMIC_SPMI_DATA (0.431) (1.8V)



PMIC_SPMI_CLK
PMIC_SPMI_DATA



PMIC_SPMI_CLK
PMIC_SPMI_DATA

PMIC - CPU
(OSCILOSCOPIO)

OSCILOSCOPIO
SPMI_CLK
SPMI_DATA

CPU
Y
NAND
OSCILOSOCPIO

LINEA RESET Y PS_HOLD



CPU Y NAND

